

Building a Complete FSK Modem Using XR-2211 and XR-2206

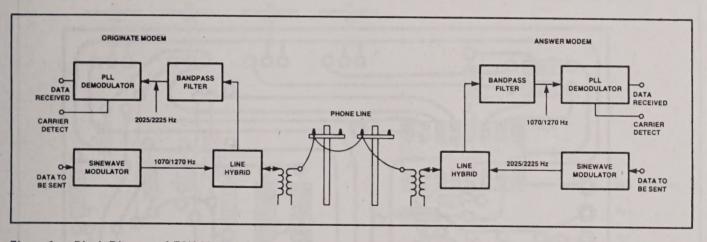
INTRODUCTION

With the number of digital systems and equipment growing so rapidly, the need for a method of moving data has also become a fast growing field. This application note describes the construction of a modem system using frequency-shift keying, FSK, for serial data transmission. The system utilizes the XR-2206 as a modulator, the XR-2211 as a demodulator. and an XR-084 op amp as a bandpass filter. These three IC's make up a complete working 300 baud, full-duplex, FSK modem.

PRINCIPLES OF OPERATION

Figure 1 shows the block diagram of an FSK system. The complete system is comprised of an answer and originate modem. The answer modem will convert input data to either 1070 Hz ro 1270 Hz, and send it to the phone line, while decoding the 1's and 0's (2025 Hz and 2225 Hz) received from the line. The originate modem simply reverses the frequencies for send and receive. The sine wave modulator will produce two discrete frequencies at its output corresponding to a one or a zero at its data input. The line hybrid will steer these frequencies to the phone line, while causing the received frequencies to go to the bandpass filter and demodulator. This block will, therefore, provide isolation between modulator and demodulator at each end. The bandpass filter is used to remove unwanted signals and noise received from the phone line before they reach the demodulator.

The PLL demodulator will lock onto incoming frequencies at its input, and produce 1's or 0's at its output. The carrier-detect output will produce a low (zero) signal when valid data is being received.



Block Diagram of FSK Modem System

OPERATION AND CALIBRATION

The circuit has been designed for +12V operation. The data inputs accept TTL-compatible signal levels, while the outputs provide OV to +12V signal levels.

Calibration is done by first adjusting the modulator. With a low-signal on its input, R21 is adjusted for 1270 Hz, or 2225 Hz for originate and answer, respectively. Then with a high-signal in, R22 is adjusted for 1070 Hz or 2025.

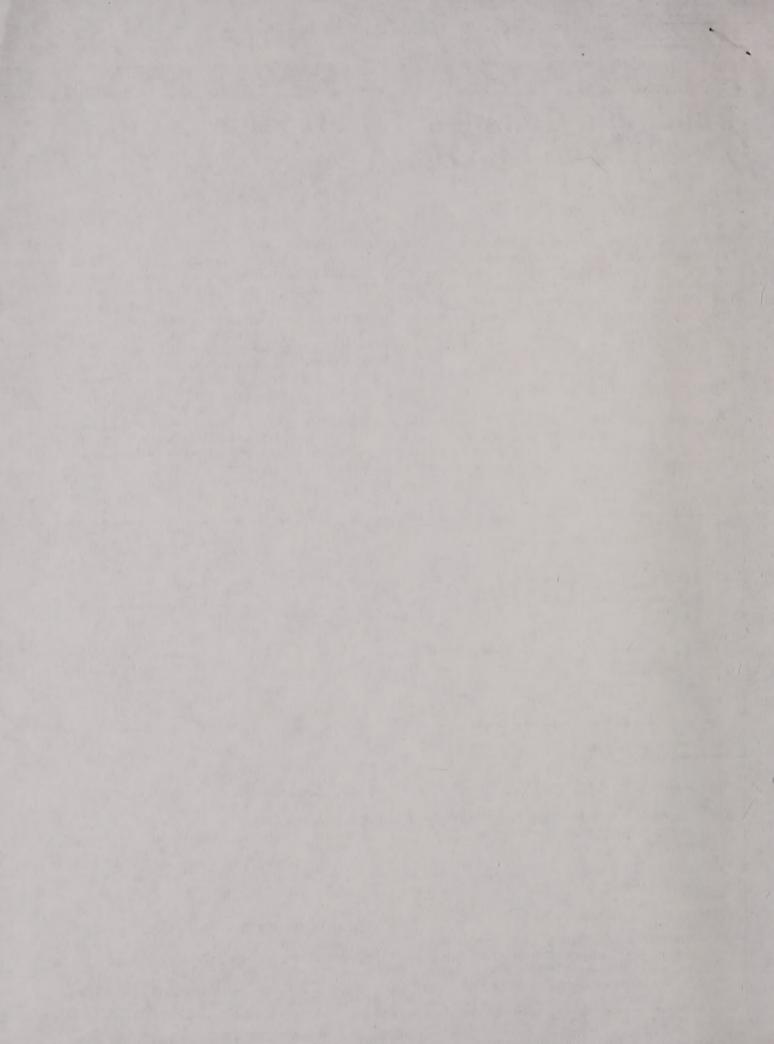
The demodulator is easily adjusted by feeding an alternating 1070/1270 Hz or 2025/2225 Hz signal into the modem input in a square wave fashion. The modulating frequency should be 150 Hz, which is one-half the system baud rate

of 300. The baud rate refers to the number of bits per second which can be sent and received. The answer can be used to drive the originate, and vice-versa. R19 is then adjusted for a square wave on the data received output.

R₂₀ is used to set the modulator output level. With the modulator output set at -6 dBm, the system will operate with an input signal range of +10 dBm to -48 dBm.

CIRCUIT CONSTRUCTION

Figures 2 and 3 show the circuit schematic and component layout. One PC board is used for answer or originate, and should use the appropriate components as listed in Table 1.



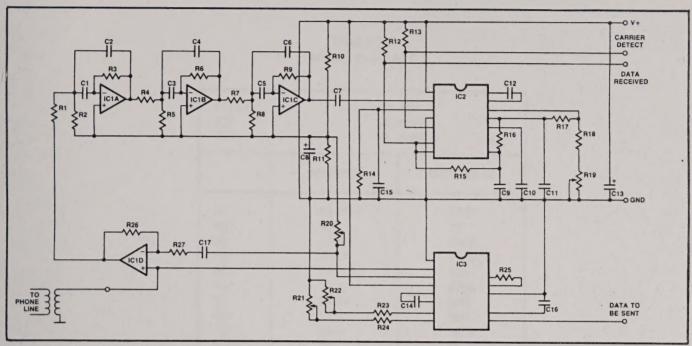


Figure 2. Complete FSK Modem Using XR-2211 and XR-2206

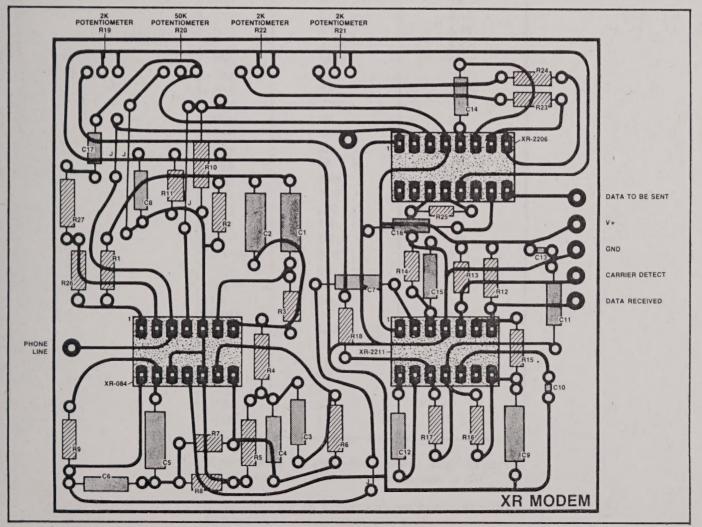


Figure 3. XR Modem Foil Side Shown (Not to Scale.)

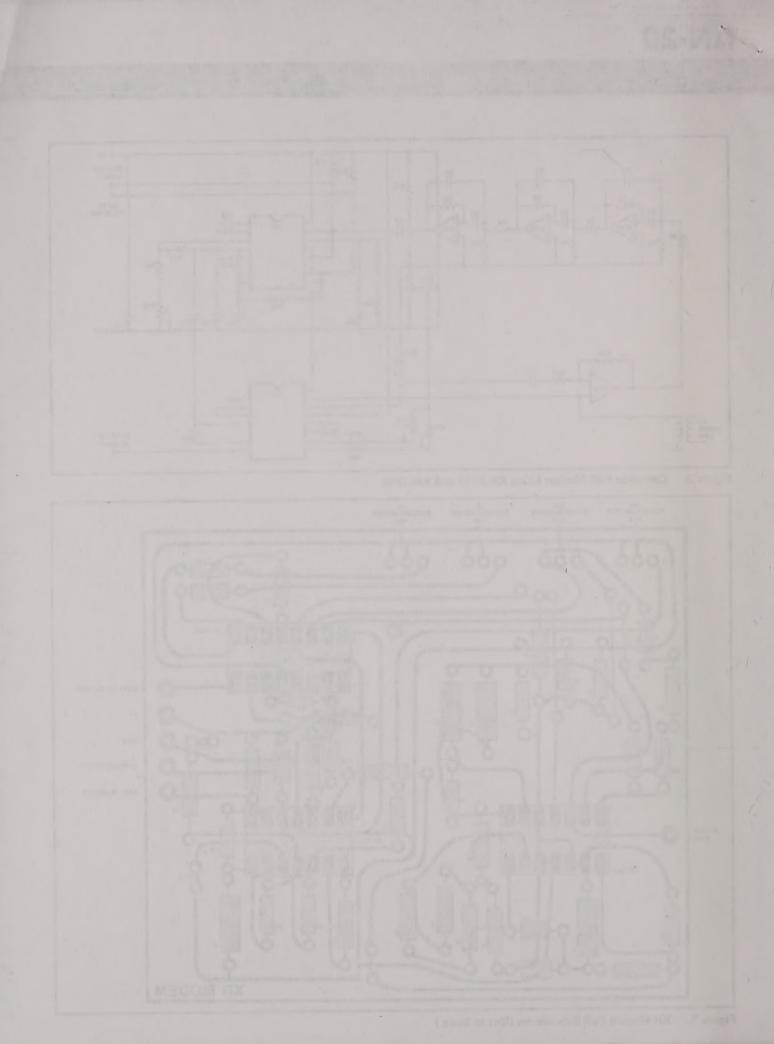


TABLE 1. Modem Parts List

IC1A-D IC2 IC3	XR-084 XR-2211 XR-2206	
	ANSWER	ORIGINATE
R1* R2* R3* R4* R5* R6* R7* R8* R9* R10 R11 R12 R13 R14 R15 R16 R17 R18 R19 R20 R21 R22 R23 R24 R25 R26 R27 C1-C6* C7 C8 C9 C11 C12 C13 C14 C15 C16 C17	40.2K 499 270K 60.4K 680 383K 24.9K 1.21K 160K 1K 15.1K 5.1K 5.1K 5.10K 510K 100K 47K 7.5K 2K 20K 20K 3.9K 3.6K 200 1M 1M 0.01 0.1 22 0.01 0.1 0.1 0.1 0.1	47.5K 191 357K 39.4K 160 270K 20K 360 160K 1K 1K 5.1K 5.1K 5.1K 5.1OK 510K 100K 100K 9.1K 2K 50K 2K 2K 8.2K 6.8K 200 1M 1M 0.01 0.1 22 0.01 0.1 0.047 1 0.1 1

All resistors are 1/4 watt -5% tolerance, except as marked with (*) which are 1% tolerance. Values given in (Ω) .

All capacitors are 5% tolerance, except as marked with (*) which are 1% tolerance. Values given in μF .

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